

Alexander Hanzlik, PhD MSc MBA

1030 Wien, Apostelgasse 39 +436804412189 ahanzlik@gmx.at www.alexanderplatz.at

CURRICULUM VITAE

Personal Information

Name	Alexander Hanzlik
Date of birth	26.03.1970, Vienna, Austria
Marital status	unmarried
Nationality	Austria
Military service	done (1989)



School Education

1976-1980 Elementary school, Vienna
1980-1988 Secondary school with bias on Latin and Greek, Vienna

Higher Education

2009-2011 **Master of Business Administration MBA**, PEF Private University of Management, Vienna
2001-2004 **PhD in Computer Science**, Vienna University of Technology
1989-1995 **MSc in Computer Science**, Vienna University of Technology

Professional Life

2015-today **Siemens Corporate Technology CT**
Senior Software Engineer
2009-2014 **Austrian Institute of Technology**, Safety and Security Department
Software Developer and Researcher
1998-2009 **Siemens Program- and System Development PSE**
Software Developer and Project Manager
1995-1998 **Frequentis Group**
Software Engineer

Other

Languages German (native speaker), Englisch (fluent in speech and writing)
Driving License (automobile)

CURRICULUM VITAE (DETAILS)

School Education

- 1976-1980 Elementary school, Vienna, A-1130
1980-1988 Secondary school with bias on Latin and Greek, Vienna, A- 1030
Graduation **with distinction**

Higher Education

- 2009-2011 **Master of Business Administration MBA**, PEF Private University of Management, Vienna
Master Thesis "Assessment of the Potential for Cost Savings and Quality Improvements by Integration of Simulation Environments into Automotive Development Processes"
Graduation **with distinction**
- 2001-2004 **PhD in Computer Science**, Vienna University of Technology
PhD Thesis "Investigation of Fault-Tolerant Multi-Cluster Clock Synchronization Strategies by Means of Simulation"
Promotion **with distinction**
- 1989-1995 **MSc in Computer Science**, Vienna University of Technology
Master Thesis "Entwurf und Implementierung von Patientendatenbank und Wissensbasis für das medizinische Expertensystem CADIAG-4" (German)
„Design and Implementation of Patient Data Base and Knowledge Base for the Medical Expert System CADIAG-4“

Professional Life

- 2015-today **Siemens** Corporate Technology **CT**
Senior Software Engineer

Projekt Work

Project iSSN (intelligent **Secondary Substation Node**): **Design and Implementation** of a remote maintenance solution for secondary substations in low-voltage grids in **Java** (Partner: ASCR **Aspern Smart City Research**)
Project WoS (**Web of Systems**): **Architecture** of systems for the Internet of Things (IoT); **Implementation** of the component "Device Configuration Management" in **C++ and Java**

Requirements Engineering

- 2009-2014 **Austrian Institute of Technology**, Safety and Security Department
Software Developer and Researcher

Research

Simulation model design for **network architectures** and electronic **control systems** for the next generation of **electric vehicles**

Supervision of degree candidates

Projekt Work

Project POLLUX: **Implementation** of the DTF Data Time Flow **Simulator** for Design and Validation of electronic control systems for **automotive applications** (Partner: PSA group, Fiat)

Project MBAT: **Design** and **Analysis** of an electronic braking system for trucks (Partner: Volvo)

Project CRYSTAL: **Development** of an automatic speed limitation system for trucks (Partner: Volvo)

Project VASL: Visual Airborne Self Localisation of drones by means of image processing methods

1998-2009 **Siemens** Program- and System Development **PSE**
Software Developer and Project Manager

Project Management

Project COSMO: Diagnosis and Analysis of DSL networks (Partner: Infineon/German Telecom)

Project BOAS: Prototype study for data rate optimization in DSL networks

Sub-project Management

Project NTS10 EWSD Internode: **Design** and **implementation** of a subscriber-side **network termination** unit (analogue part)

Projekt Work

Project QHCU: **Software development** for an **Electronic Control Unit ECU** for **vehicle dynamics management** (Partner: Magna/Audi)

Project SM@RT: **Firmware development** for a touch panel for industrial production control under **µC-Linux**, based on an **ARM** architecture (Partner: Siemens Automation and Drives)

Project NetTool: **Development** of a rule based tool for encoding/decoding of communication protocol stacks in **Java** and **XML**

Autonomous development activities for internal and external customers (ASFINAG, Infineon, German Telecom)

Desktop application development for different operating systems (**Windows/Linux/UNIX**) and programming languages (**C/C++, C#, Java**)

Firmware development for **Embedded Systems** (C/C++)

Writing of **Specifications** and **Documentations**

Integration test execution at customer sites

Training

Siemens PROMOTE Project Manager Curriculum: **Junior Project Manager**, Primas CONSULTING

Siemens internal trainings: Development Methods, Function Point Analysis, Requirements Engineering

How to work with Tornado, Windriver Systems

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1995-1998 **Frequentis Group**

Project ARTEMIS: Software development for an air traffic control voice communication system (Customer: French Air Navigation Systems **STNA**)

Activities

Software Development (C)

Factory Acceptance Test execution with the customer

Planning and **estimation** of efforts for sub-projects and work packages

Preparation and Execution of **Code reviews**

Writing of **Specifications** and **User manuals**

Research Activities

2003-2007 **Research Assistant**, Institute for Computer Engineering
Project "Clock Synchronization Strategies", funded by Austrian Science
Fund FWF (Project P16638-N04)

since 2005 Constant **publication activities**. Topics: Clock synchronization in fault-tolerant distributed real-time systems, Investigation of synchronization quality and fault-tolerance capabilities of real-time protocols (FlexRay, TTP/C, Time-Triggered Ethernet), Simulation of distributed real-time applications, Concepts for simulation of electronic control systems for automotive applications

2002-2009 Design and Implementation of SIDERA, an integrated simulation environment for fault-tolerant real-time systems based on the time-triggered model of computation